CSE TERMINAL MASTERS PLAN OF STUDY in VLSI

Stu Name: ___________________________ UMID: ___________________________

Advisor (signature required) ___________________________ Date: ____________

MSE Degree or MS Degree (circle one) ___________________________

(What is your undergrad degree field? (engineering or non-engineering) ___________________________

<table>
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<tr>
<th>Term</th>
<th>Course</th>
<th>Grade</th>
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Credit Hours: At least 30 credit hours
Technical Electives: at least 24 credit hours
CSE Courses: 500 level or above at least 15 credit hours
Cognates: At least 4 hrs. of Grad. Level course work
427 and 627 (BOTH)

VLSI Kernel Requirements:
- Two of 400 level: 482; 483; 484; 485; 487; 489; 490 or 571 or 582 or 583 or 584 or 587 or 588 or 589 or 590 or 591 (Software)
- 492 or 543 or 545 (Artificial Intelligence)
- 470 or 473 or 478 or 527 or 570 or 573 or 577 or 578 or 579 or 583 (Hardware)

Total Hours: (fill in for each column)

NOTES:
1) Cognates are courses outside the CSE Division (Courses cross-listed with CSE may not be used, and all cognates must be approved by advisor)
2) A maximum of six credit hours of individual study, research, and seminars
3) You must meet all Rackham and Program requirements (see brochures for details)
4) It is expected that most entering students will have already completed courses equivalent to (482 or 483) and 492 and (470 or 478)
5) Seminar, directed study credits (except 3 hrs. of EECS 599) do not count toward the 500 level course requirement
6) It is the student’s responsibility to see that all requirements are met.
7) You must choose 2 of the 4 areas in addition to the VLSI Kernel
8) one of the 500 level must be from the approved list/see brochure
9) If you already have a masters degree from another institution that has been deemed relevant by CSE, you are not eligible for a masters degree from this program.

for office use only:

No grades below B-
Approved MPS
Other, Masters thesis, TC

GPA | CTP

Term: ___________________________

MSE MS

1/15